

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	11	L2 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:42
L4	18	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:42
L3	6508	L2 and "24"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:41
L2	13655	(714/701,702,718-720,725,733 365/200,201,230.06 438/11).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 16:41
S17	3	"472818-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/07 11:16
S16	2	"20030167428-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/07 11:15
S15	0	"US-20030167428-\$".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/05 17:33
S14	2	"6001662".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/05 17:14

S13	24	(((((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)) and (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:39
S5	678	((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:31
S12	1188	((((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:28
S11	1188	((physical near3 address near4 (map\$4 or generat\$3)) same (logical near3 address)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:28
S10	374	((physical near3 address near map\$4) same (logical near3 address)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:25
S8	2	"5633877".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 16:23
S9	1	"5173906".PN.	US-PGPUB; USPAT	OR	ON	2004/06/04 15:58
S7	0	"5633877"".pn"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:57

S6	28	((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test built-in adj self adj test \$1BIST array adj built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit function))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:57
S4	16	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and (logical near4 address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:29
S1	329	((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:25
S3	2	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) same (physical coordinate "XY") same (logical or binary or bits)) same (row same column))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:19
S2	0	(((((configur\$4 program\$5) near5 (self-test self adj test built-in adj self-test \$1BIST array with built-in adj self-test ABIST PBIST) near6 (controller scheduler register engine circuit apparatus device unit)) same (memory \$1RAM))) and ((map\$4 or convert\$3) near4 (physical coordinate "XY") near6 (logical or binary or bits)) same (row same column))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/04 15:17